

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

First Named Inventor: HANZAWA, SATORU Art Unit: 2827

Appln. No.: 10/579,911 Examiner: Hidalgo,
Fernando N.

Filed: April 9, 2007 Conf. No.: 6903

For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

* * *

REQUEST FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby request extension of time for a third month for the instant application. Applicants have previously requested and paid for a two-month extension of time concurrent with the filing of an Amendment After Final Rejection on June 8, 2009. This additional extension period is being requested so that entry of an Examiner's Amendment can be made to place the application in condition for allowance.

The amount now due for this additional extension is \$620.00 (three month extension fee of \$1110.00 minus previously paid two month extension fee of \$490.00).

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10573) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

By:



Mitchell W. Shapiro
Reg. No. 31,568

Miles & Stockbridge, P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102-3833
(703) 610-8651

Patrick L. Miller
Reg. No. 57,502

June 24, 2009